

jc971 U.S. PTO

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02/25/02

## U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10080580	02/25/2002	257	578	2814	

\*\*APPLICANTS: Ueno Yutaka;

\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-367482 11/30/2001

PG-PUB DO NOT PUBLISH ☐RESCIND ☐Foreign priority claimed ☐ yes ☐ no35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

219975US2S

TITLE : Semiconductor device having leadless package structure

U.S. DEPT. OF COMM./PAT. &amp; TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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